

Notice of References Cited	Application/Control No. 09/917,535		Applicant(s)/Patent Under Reexamination THOMPSON ET AL.	
	Examiner CHAMELI C DAS		Art Unit 2122	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-6,651,245	11-2003	Damron et al.	717/159
*	B	US-6,282,706	08-2001	Chauvel et al.	717/150
*	C	US-5,761,515	06-1998	Barton et al.	717/158
*	D	US-6,732,235	05-2004	Krivacek et al.	711/118
*	E	US-6,578,131	06-2003	Larson et al.	711/216
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	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

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	N					
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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	TITLE: Automatic Compiler Techniques for Thread Coarsening for Multithreaded Architectures, author: Zoppetti et al, ACM, 2000.
	V	TITLE: The Performing Implications of Thread Management Alternatives for Shared-Memory Multiprocessors, author: Anderson et al, ACM, 1989.
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.